

Fig. 1

200 →

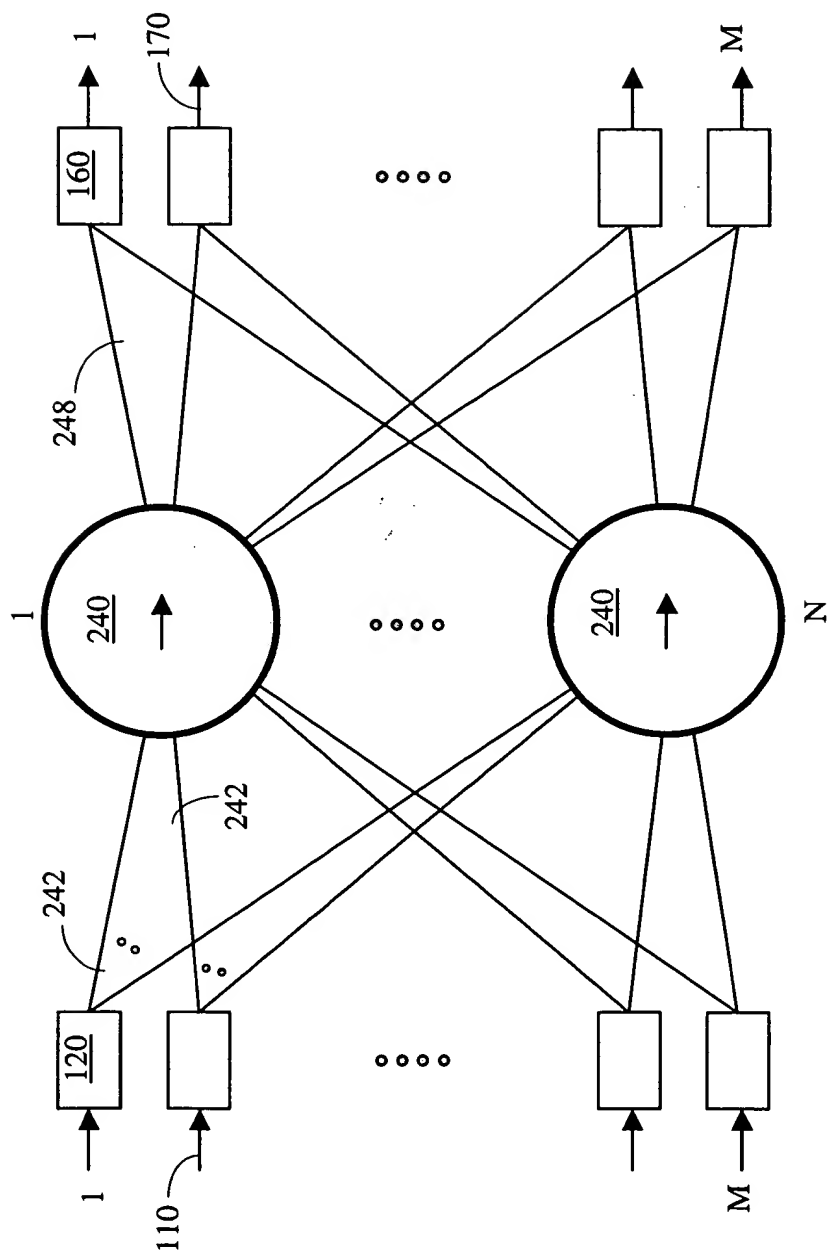


Fig. 2

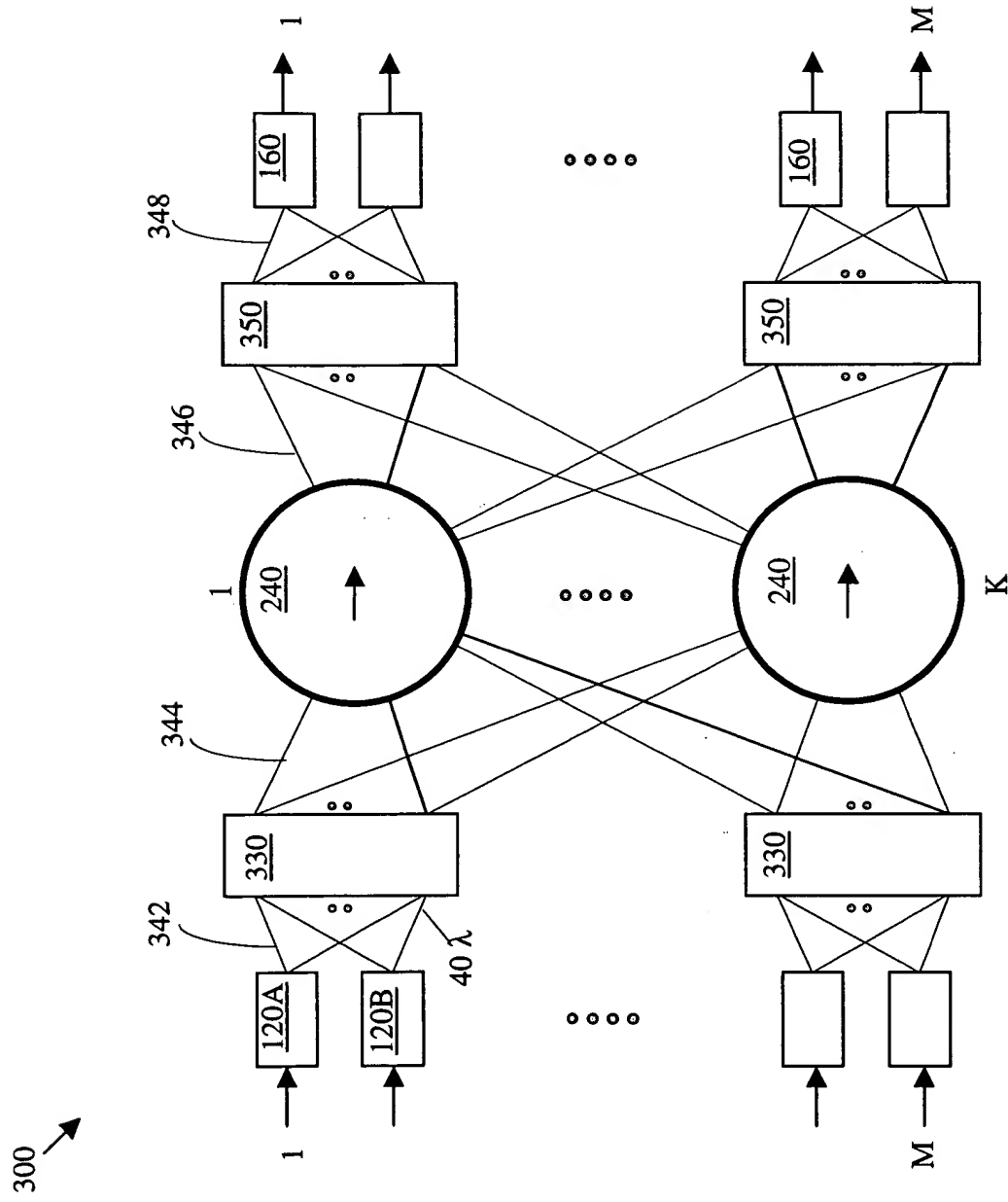


Fig. 3

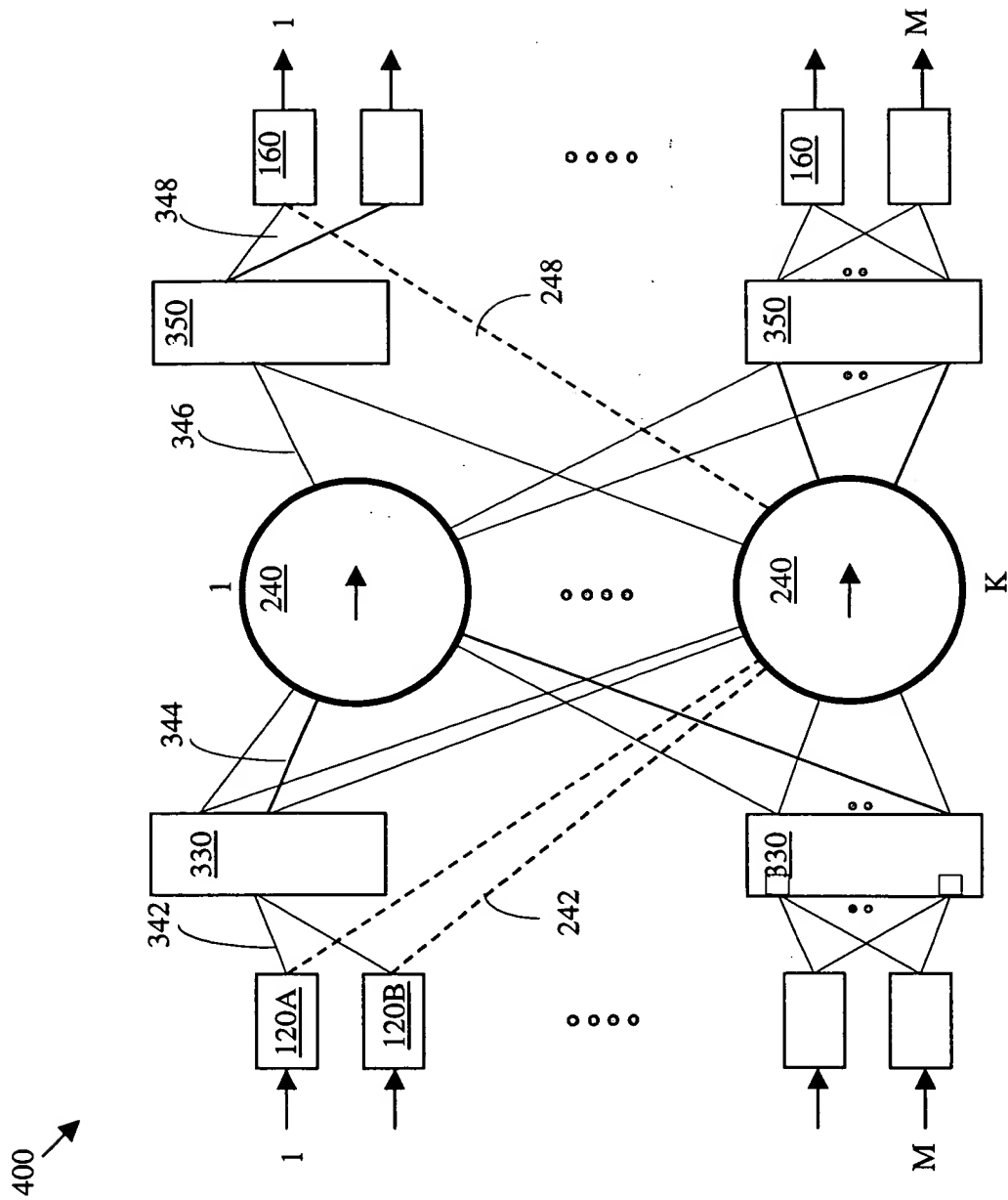


Fig. 4

FIG. 5 is a schematic diagram of a cross connector 520. The cross connector 520 has four input ports A, B, C, and D, and four output ports E, F, G, and H. The cross connector 520 is configured to connect the input ports to the output ports in a cross fashion, such that input A is connected to output F, input B is connected to output G, input C is connected to output H, and input D is connected to output E.

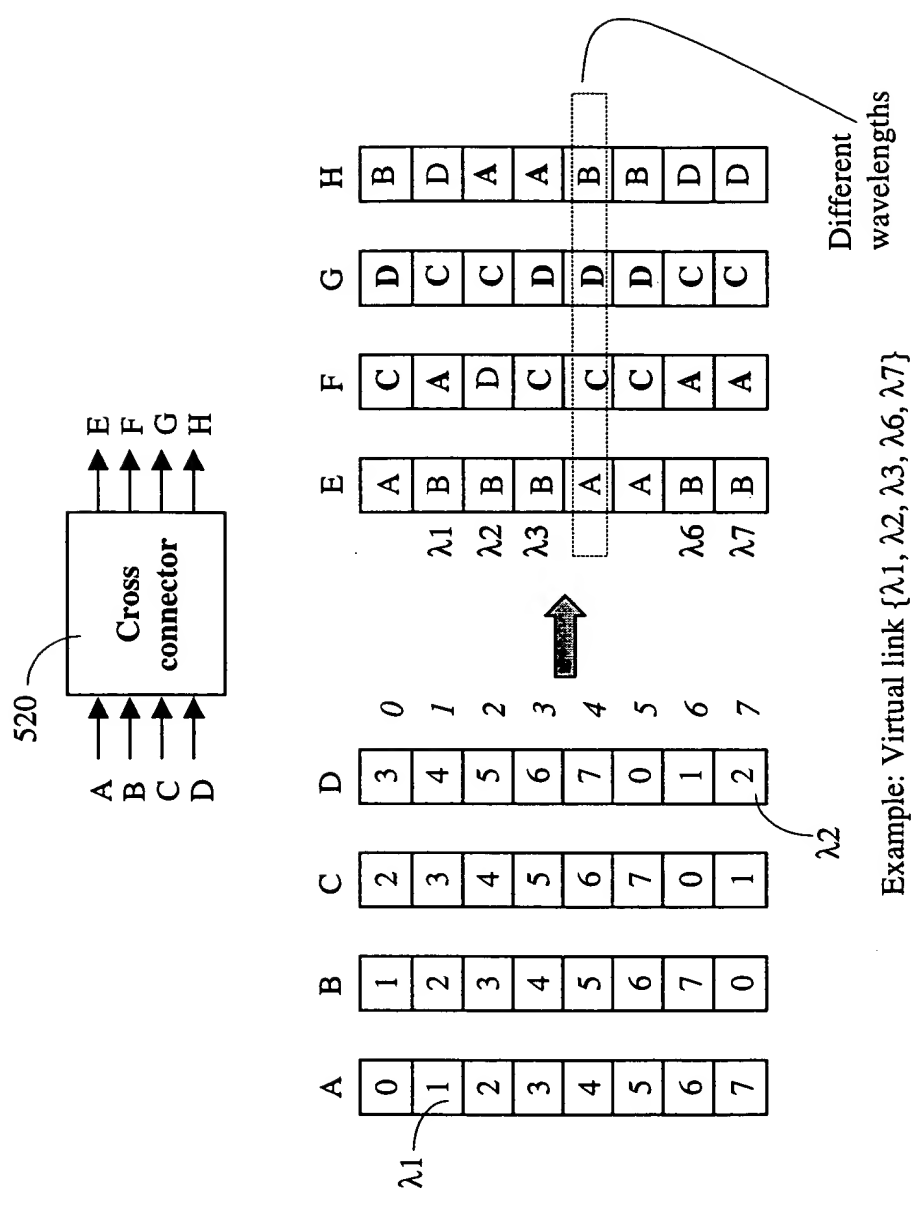


Fig. 5

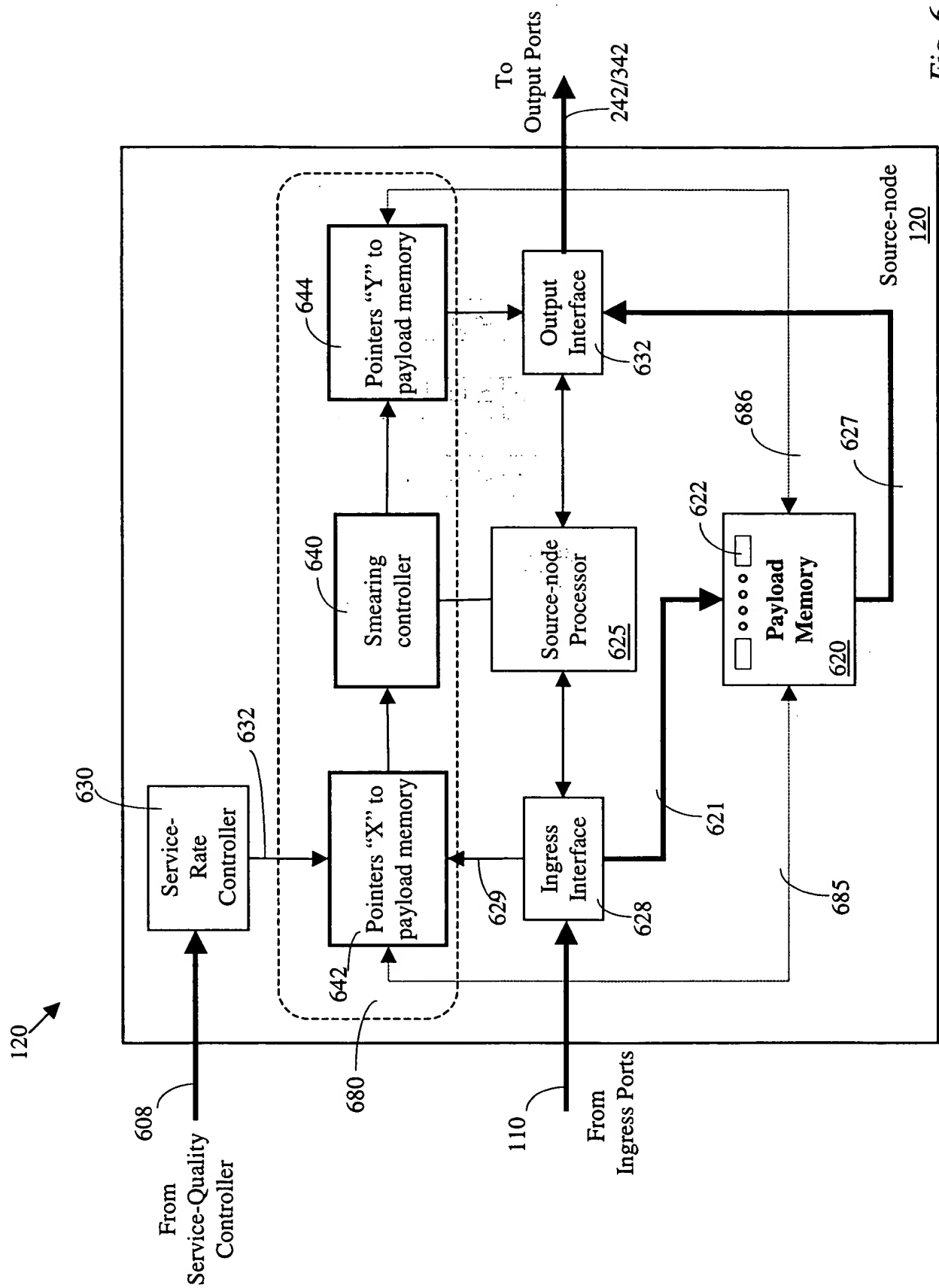


Fig. 6

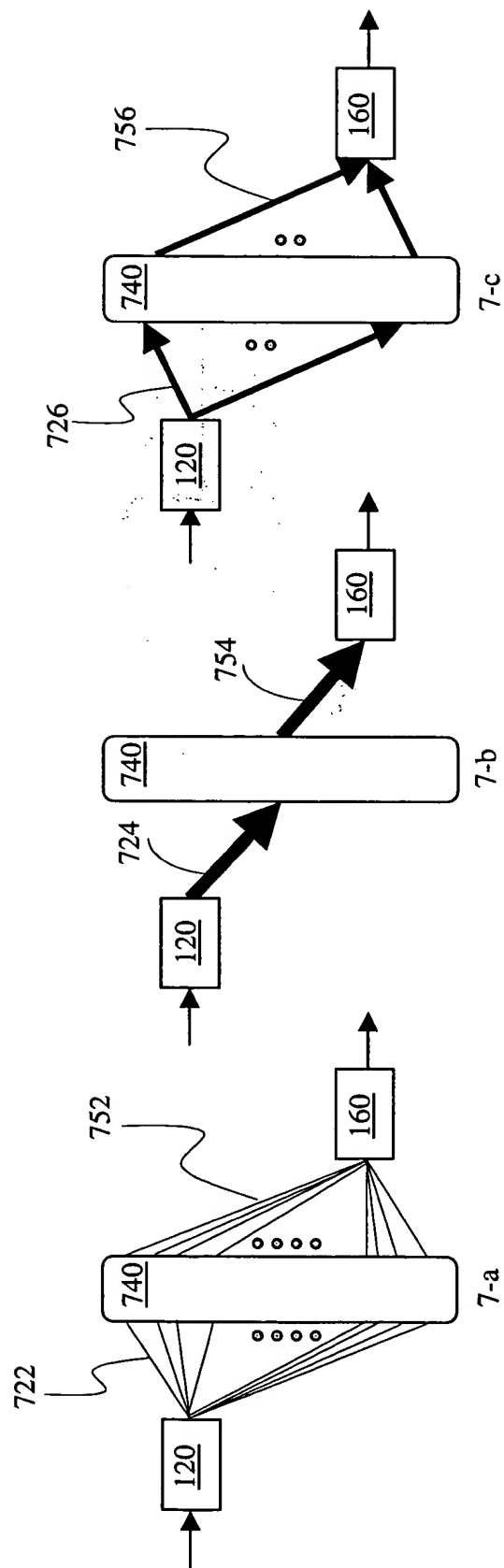


Fig. 7

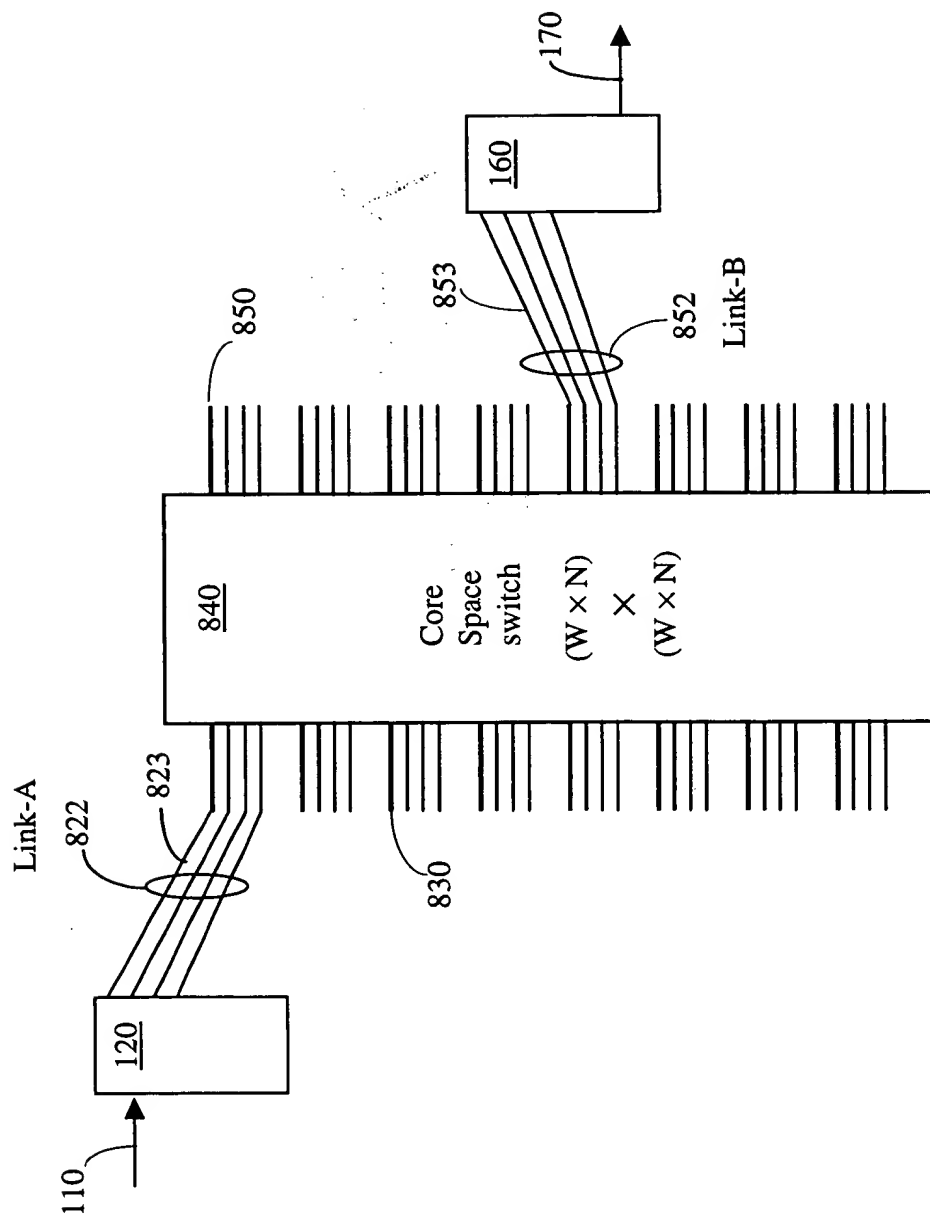


Fig. 8

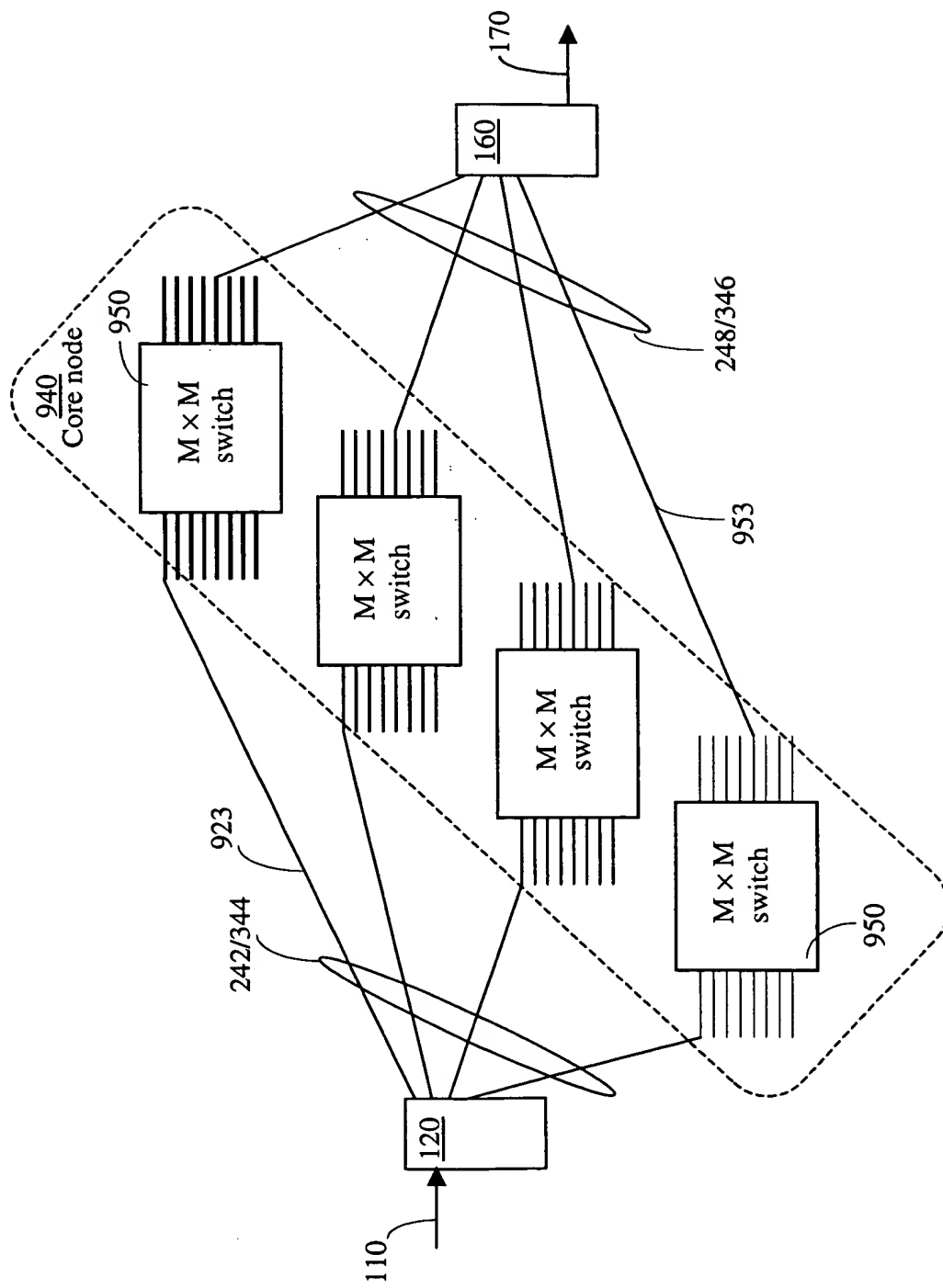


Fig. 9

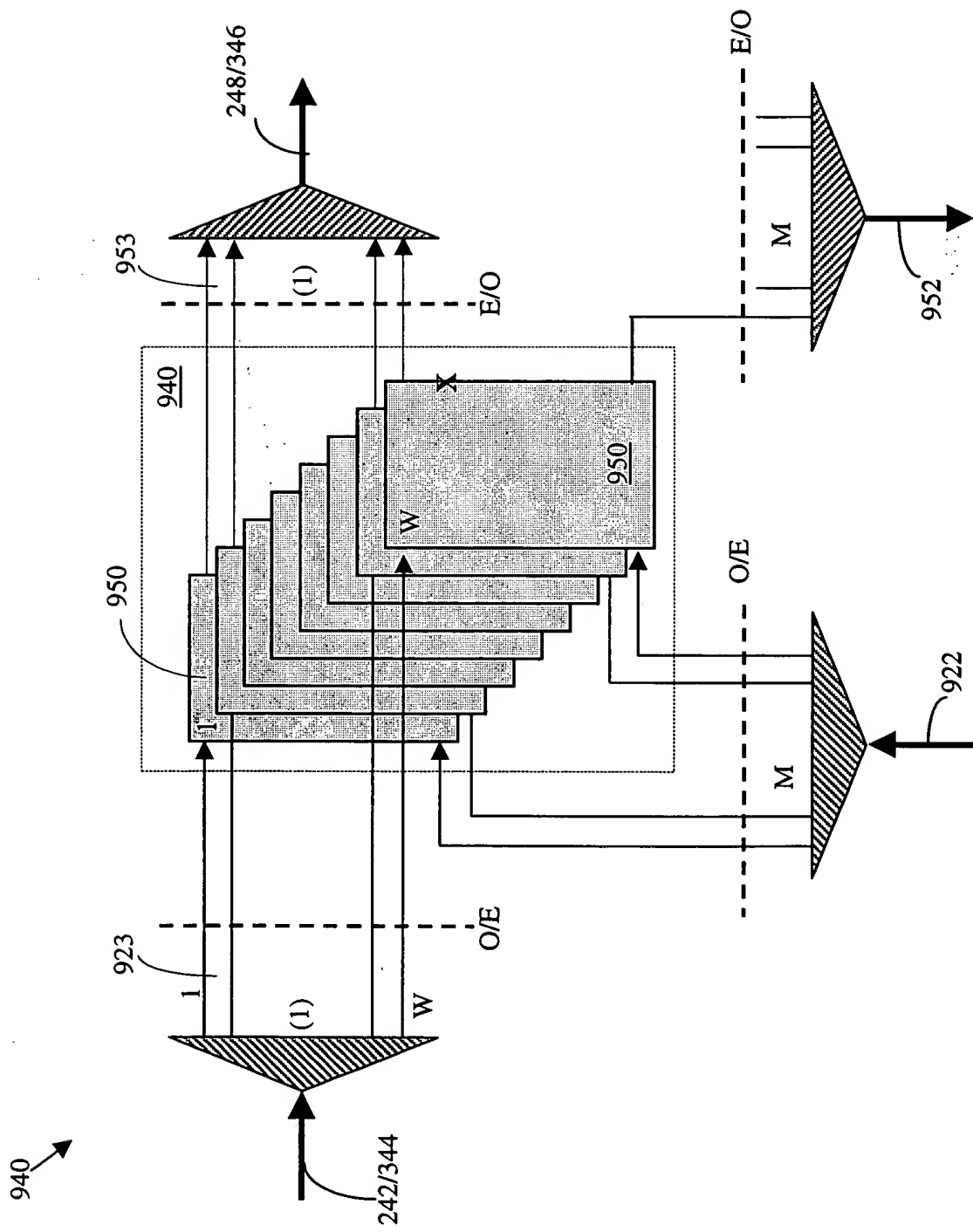


Fig. 10

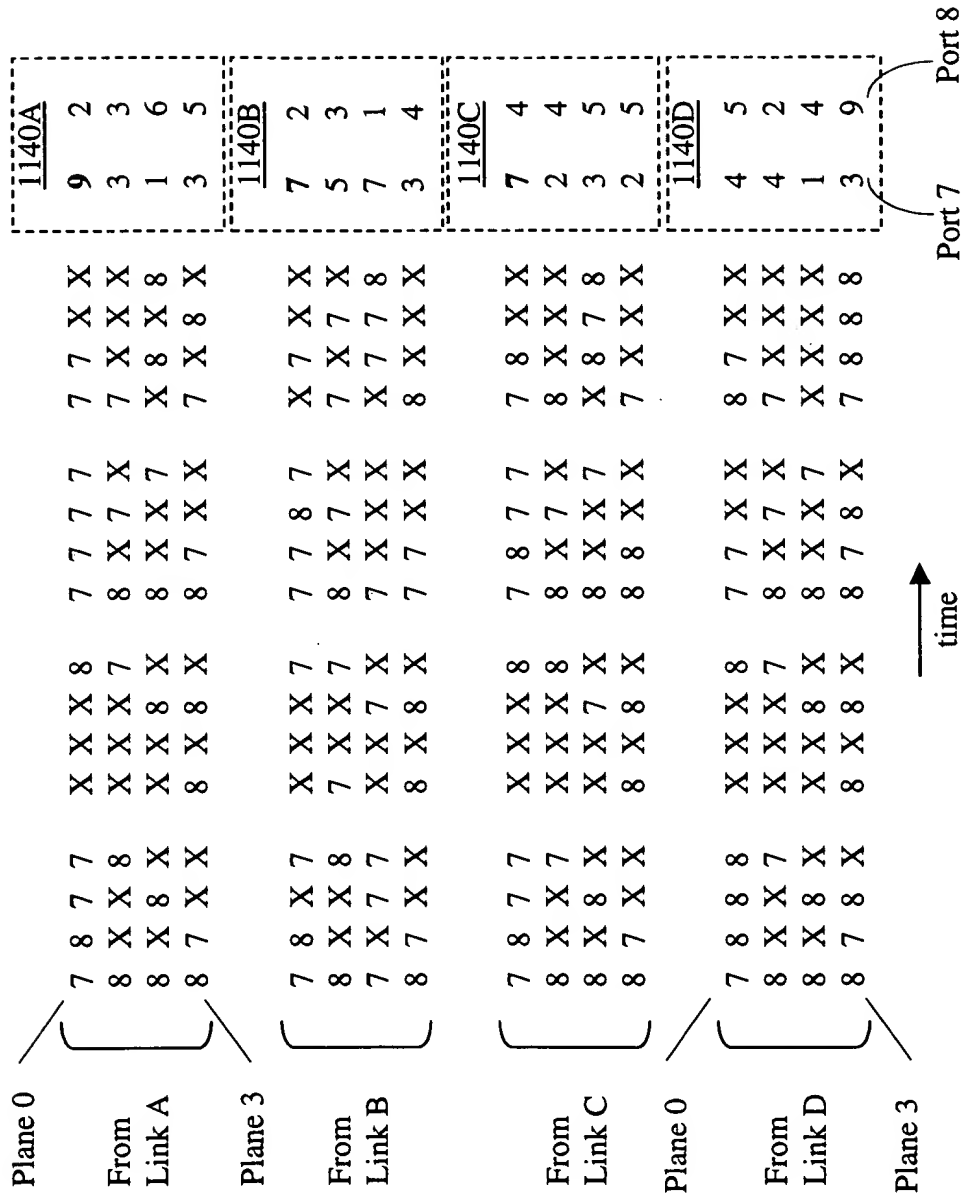


Fig. 11a

1160 ↗

	Port 7	Port 8
Link-A	16	16
Link-B	22	10
Link-C	14	18
Link-D	12	20
	64	64

Fig. 11b

1180 ↗

	Port 7	Port 8
Plane-0	27	13
Plane-1	14	12
Plane-2	12	16
Plane-3	11	23
	64	64

Fig. 11c

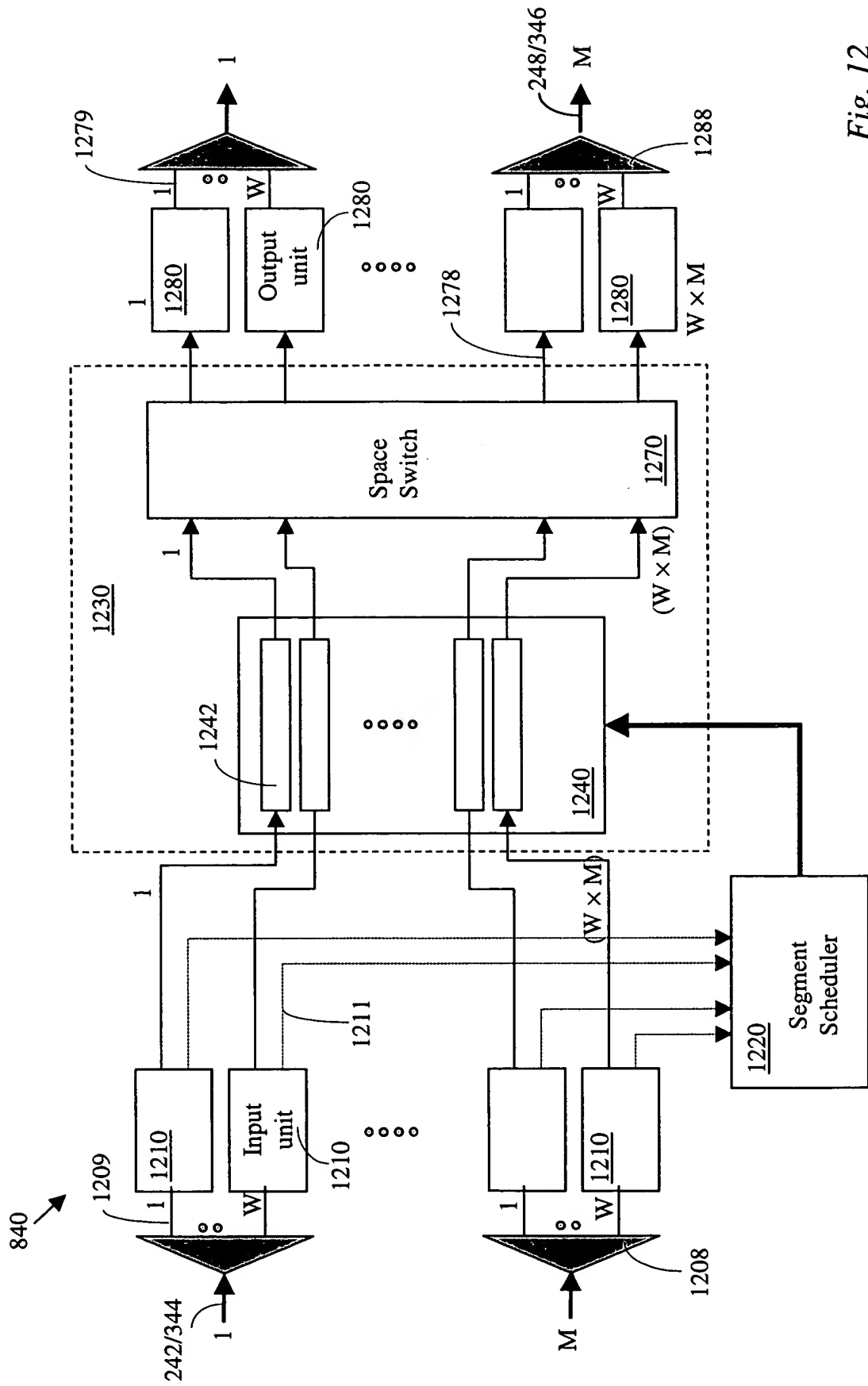


Fig. 12

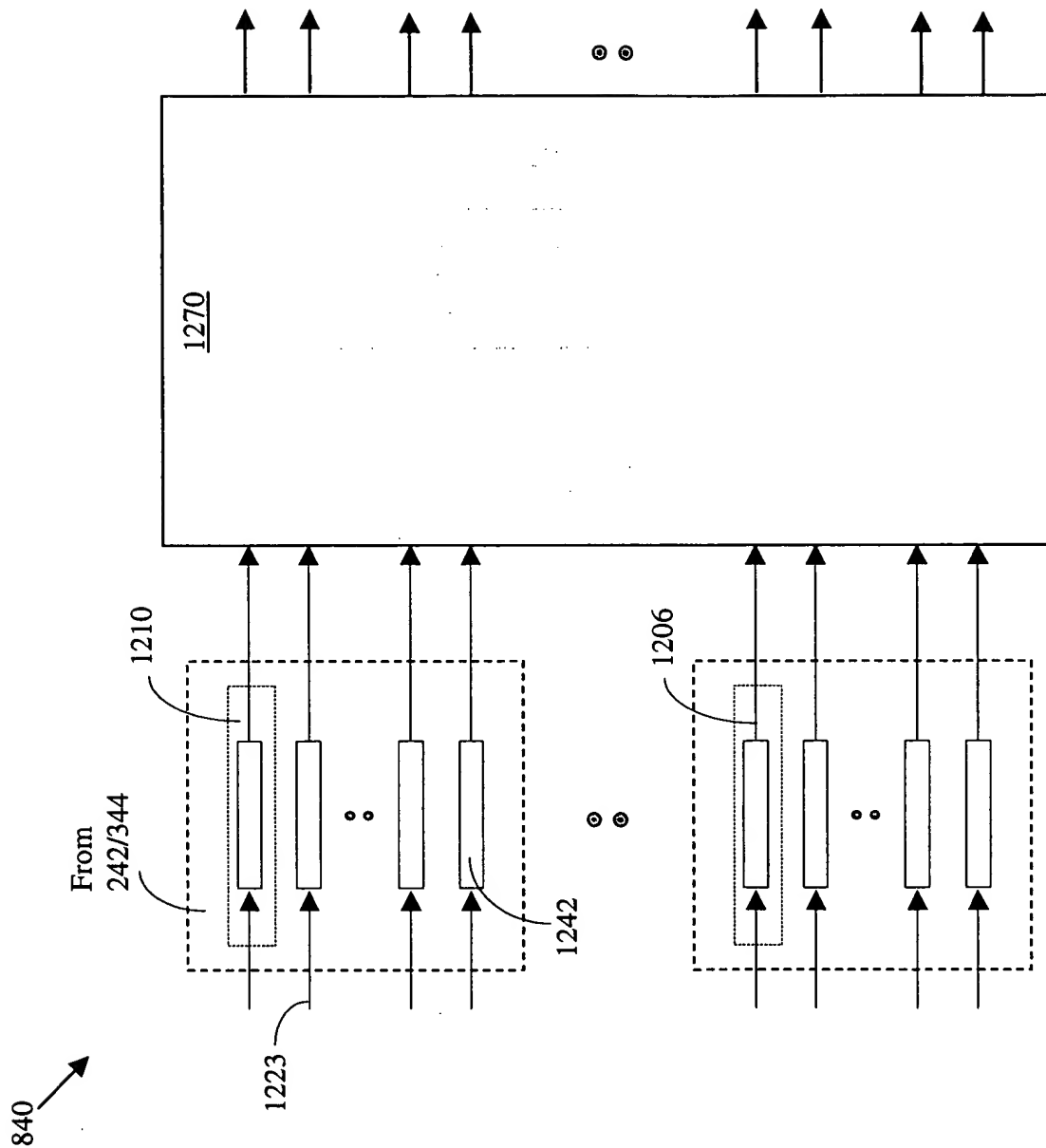


Fig. 13

940

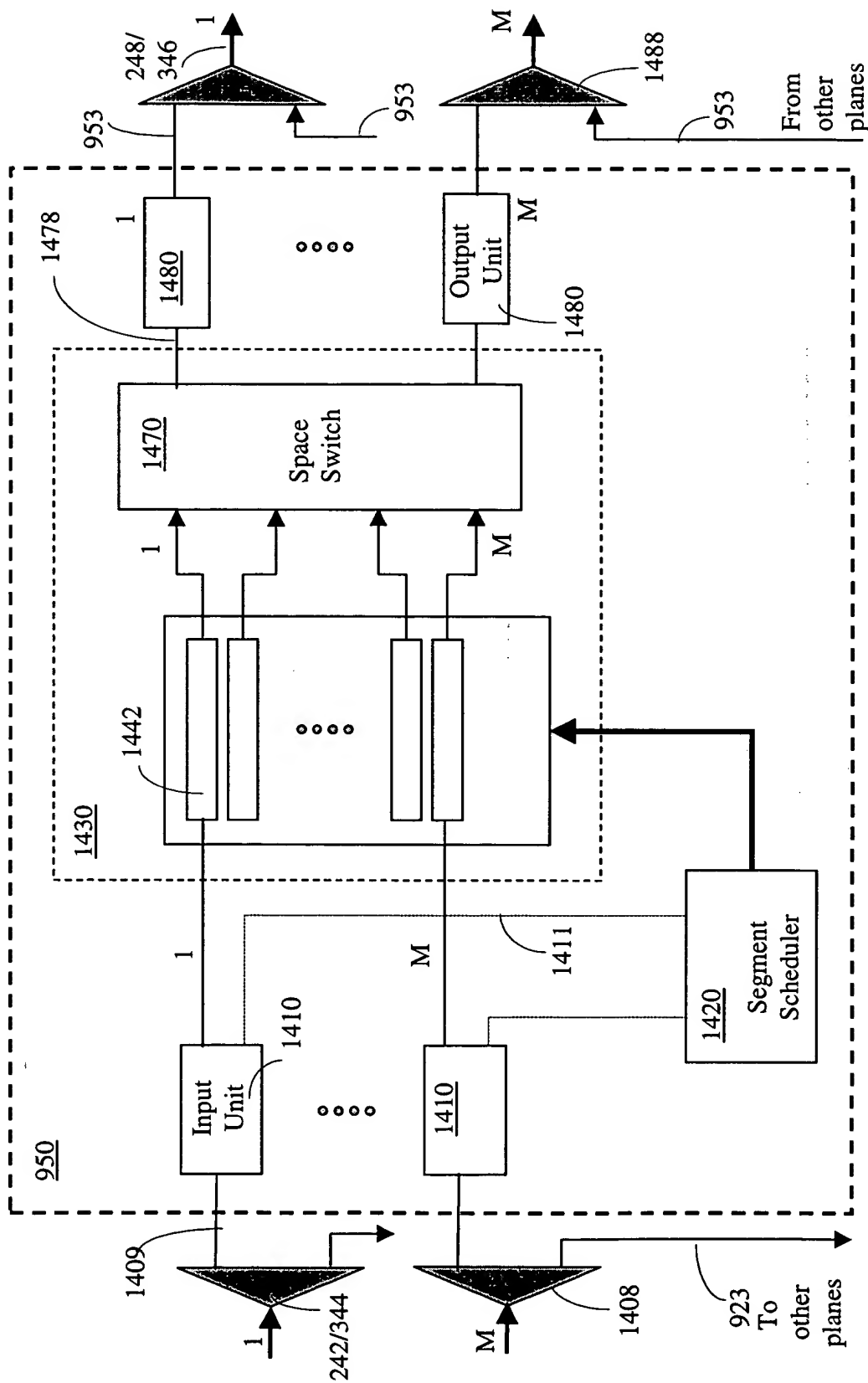


Fig. 14

FIG. 15 is a block diagram of a system 1430, which includes a first set of input devices 1442, a first processing unit 1470, a second set of input devices 1442, and a second processing unit 1478. The first set of input devices 1442 is connected to the first processing unit 1470, and the second set of input devices 1442 is connected to the second processing unit 1478. The first processing unit 1470 and the second processing unit 1478 are connected to each other via a communication bus 1476. The first processing unit 1470 and the second processing unit 1478 are also connected to a common output bus 1474, which provides output to a set of output devices 1472.

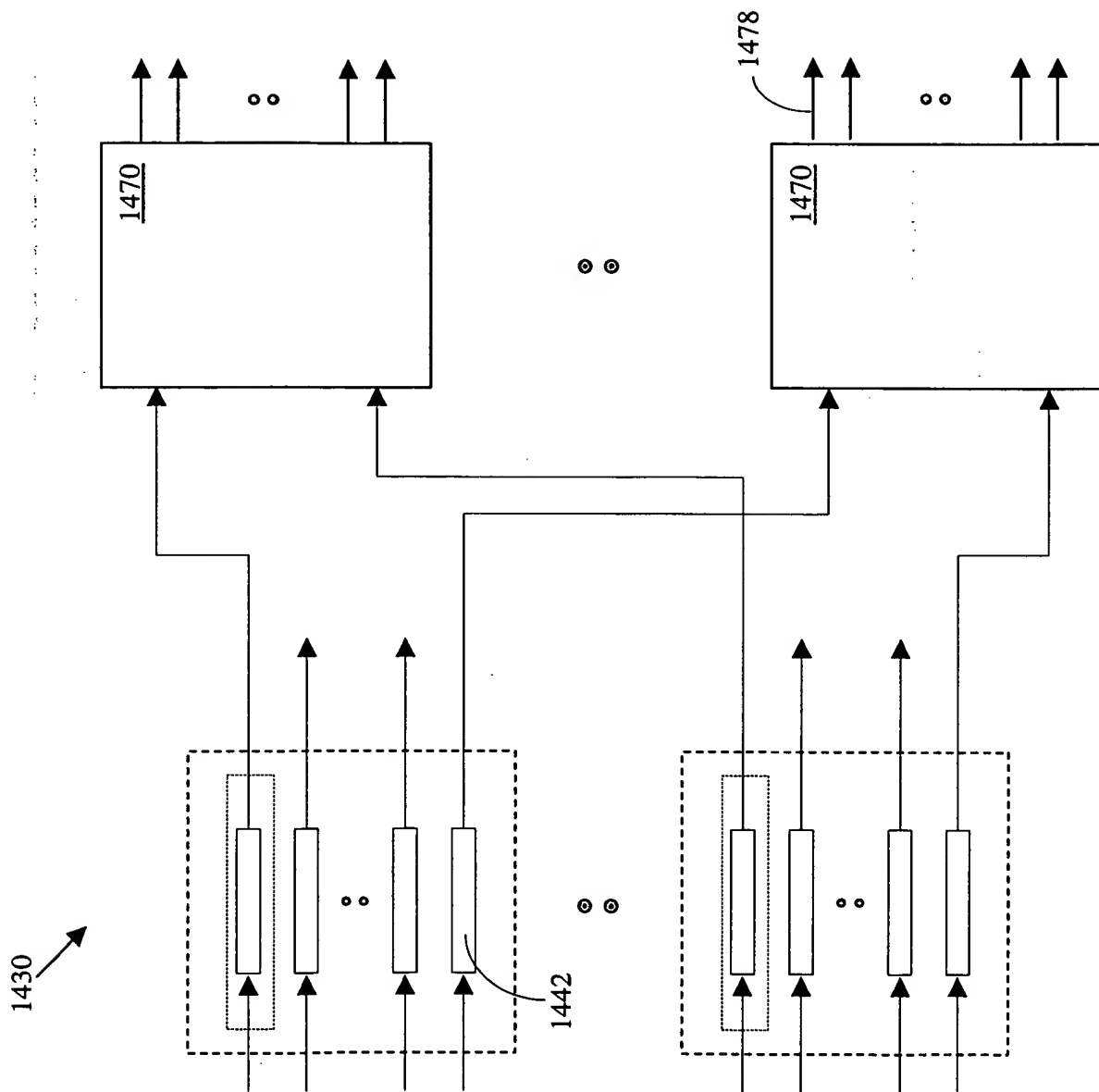


Fig. 15

Year	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100
1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100	

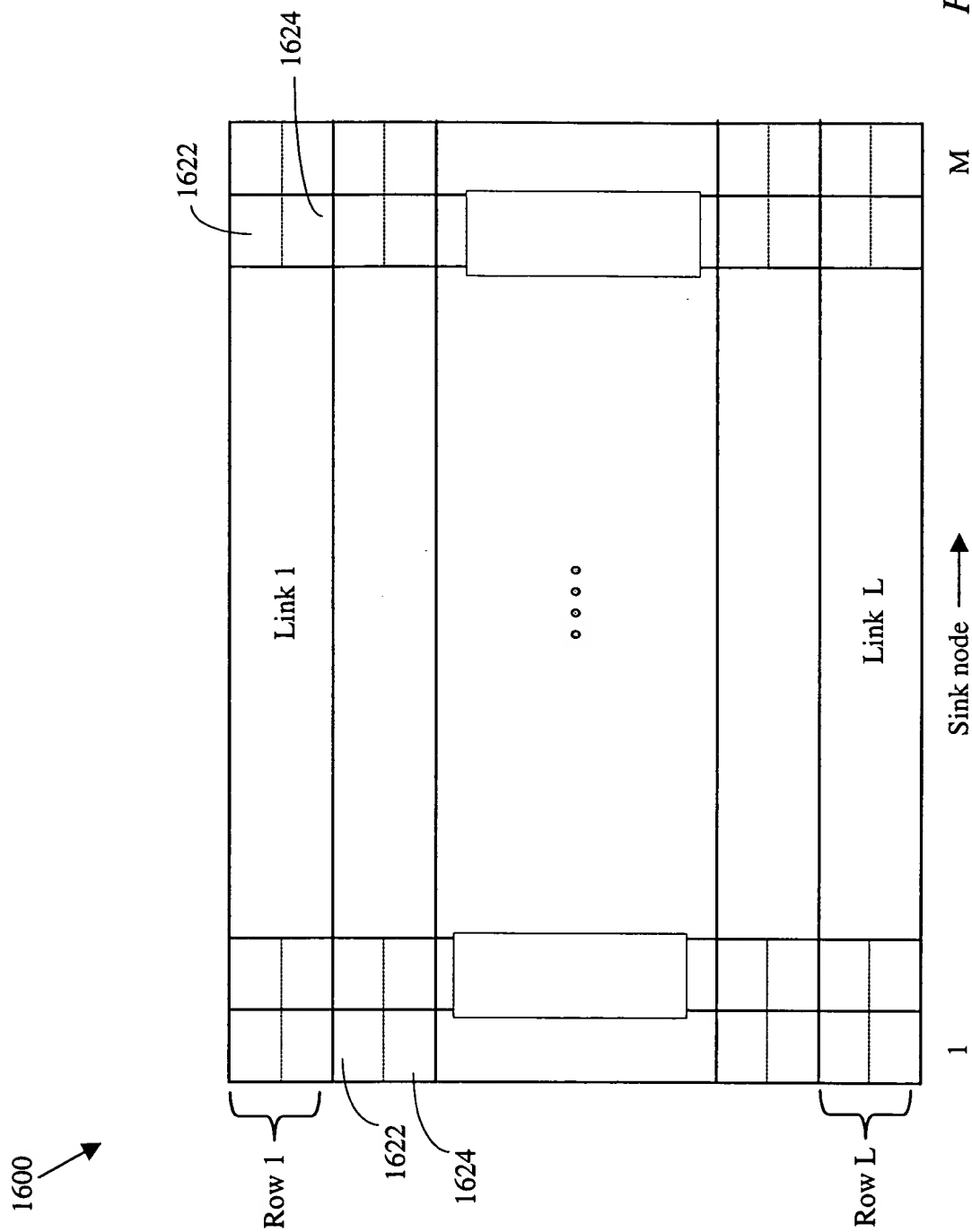


Fig. 16

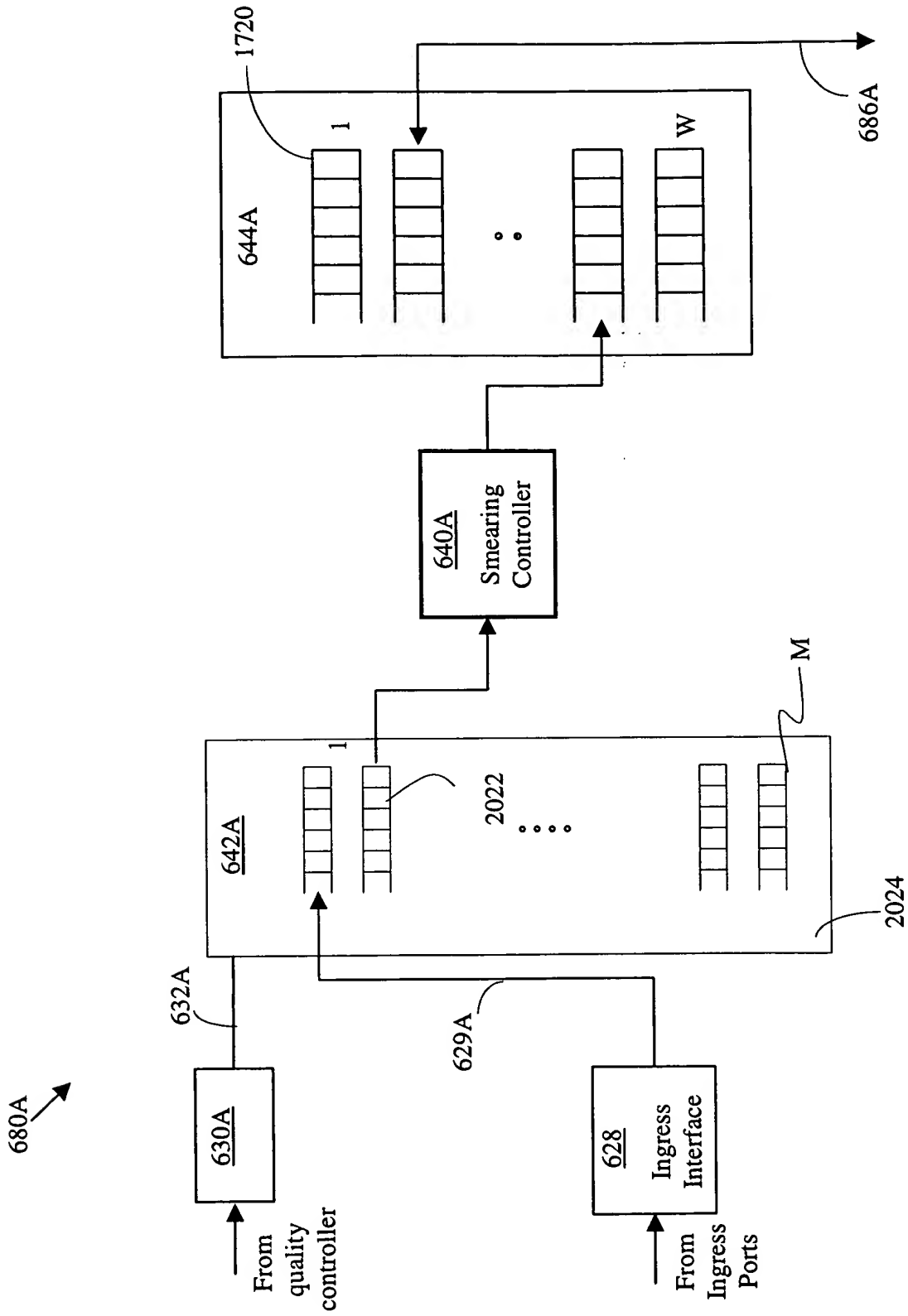


Fig. 17

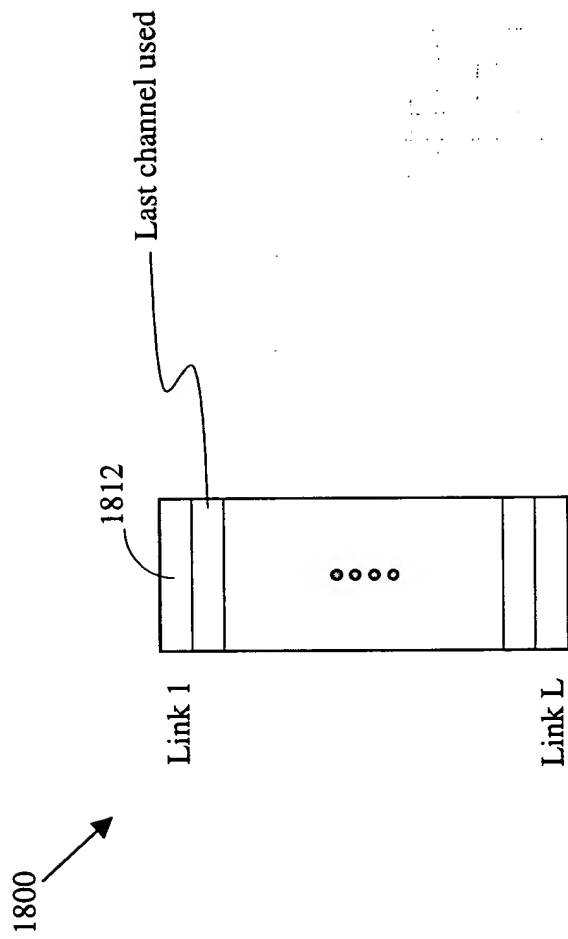


Fig. 18

FIG. 19 is a schematic diagram of a link structure 1900. The link structure 1900 includes a plurality of links 1920. The links 1920 are connected in a chain. The link structure 1900 is used in a system.

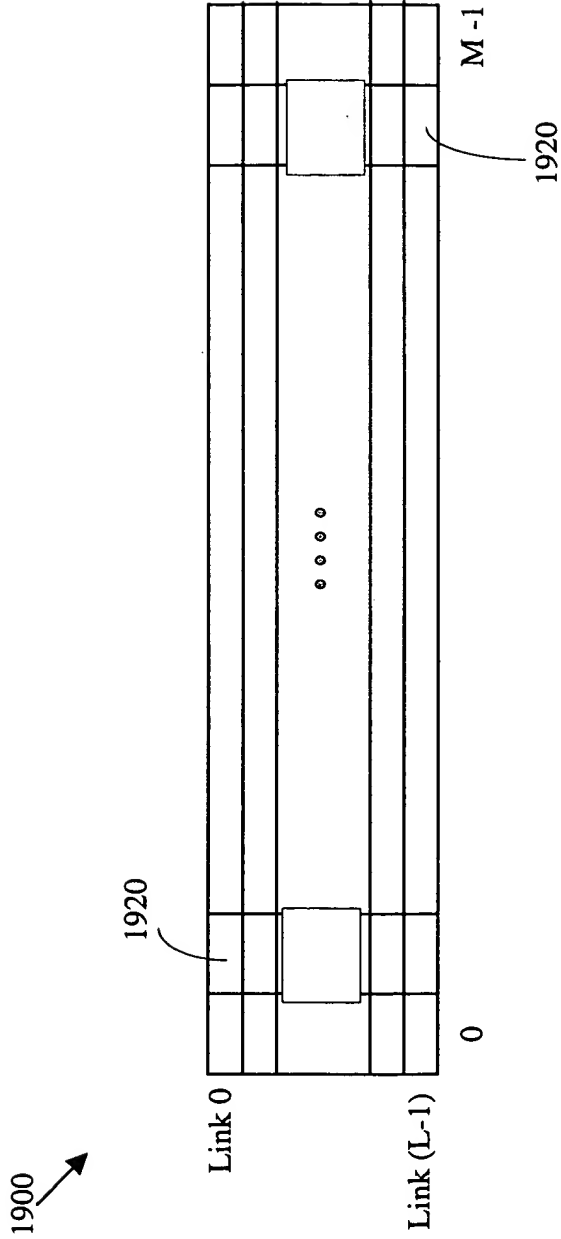


Fig. 19